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				Application Number	09/832,844
				Filing Date	April 12, 2001
				First Named Inventor	Shunpei YAMAZAKI
				Group Art Unit	2826
				Examiner Name	M. Tran
				Attorney Docket Number	0756-2288
Sheet	1	of	1		

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ² (if known)				
MLT		JP	64-010711		—	01/13/1989	—	Abst.
MLT		JP	01-236731		—	09/21/1989	—	Abst.
MLT		JP	02-067817		—	03/07/1990	—	Abst.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
MLT		H. YANAI, 3.3.2 Basic Circuit of Analog Switch, Semiconductor Handbook, 2 nd Edition, Pages 842-843, 1977.		Concise Statement

Examiner Signature	Minhloan Tran	Date Considered	9/2005
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